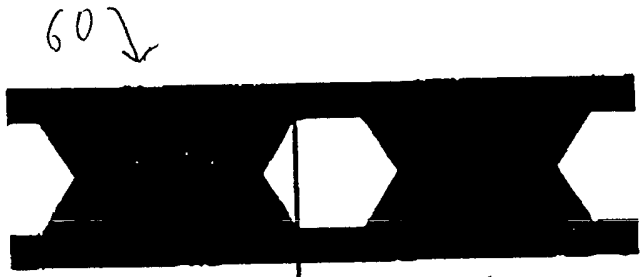
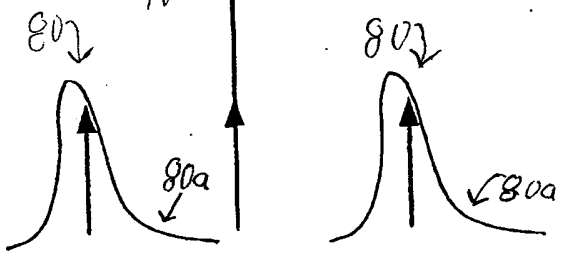
Eye diagram
FIG. 1A



Jitter histogram &
Sampling clocks
FIG. 1B



# of UP pulses &
# of DOWN pulses
FIG. 1C

"PRIOR ART"

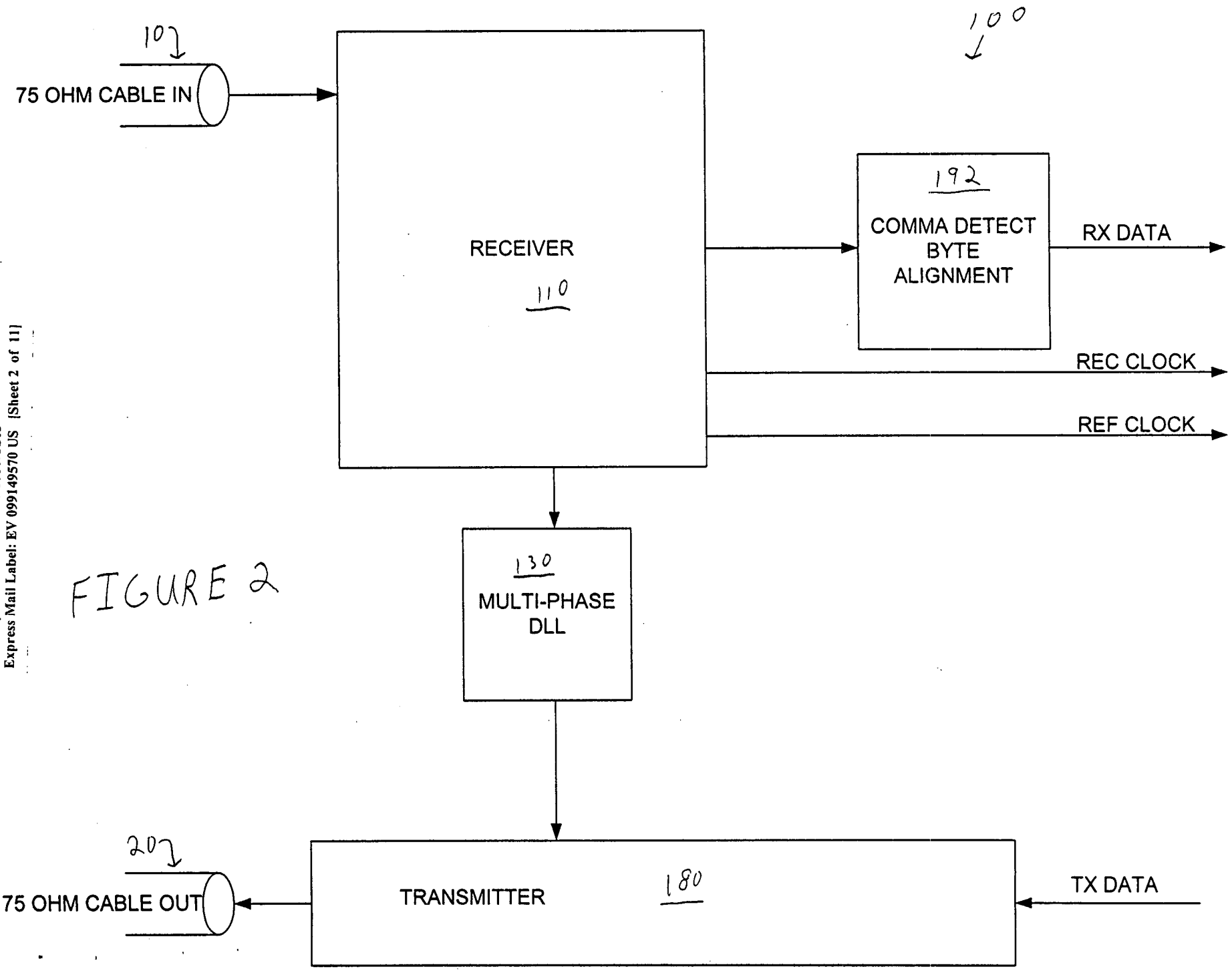
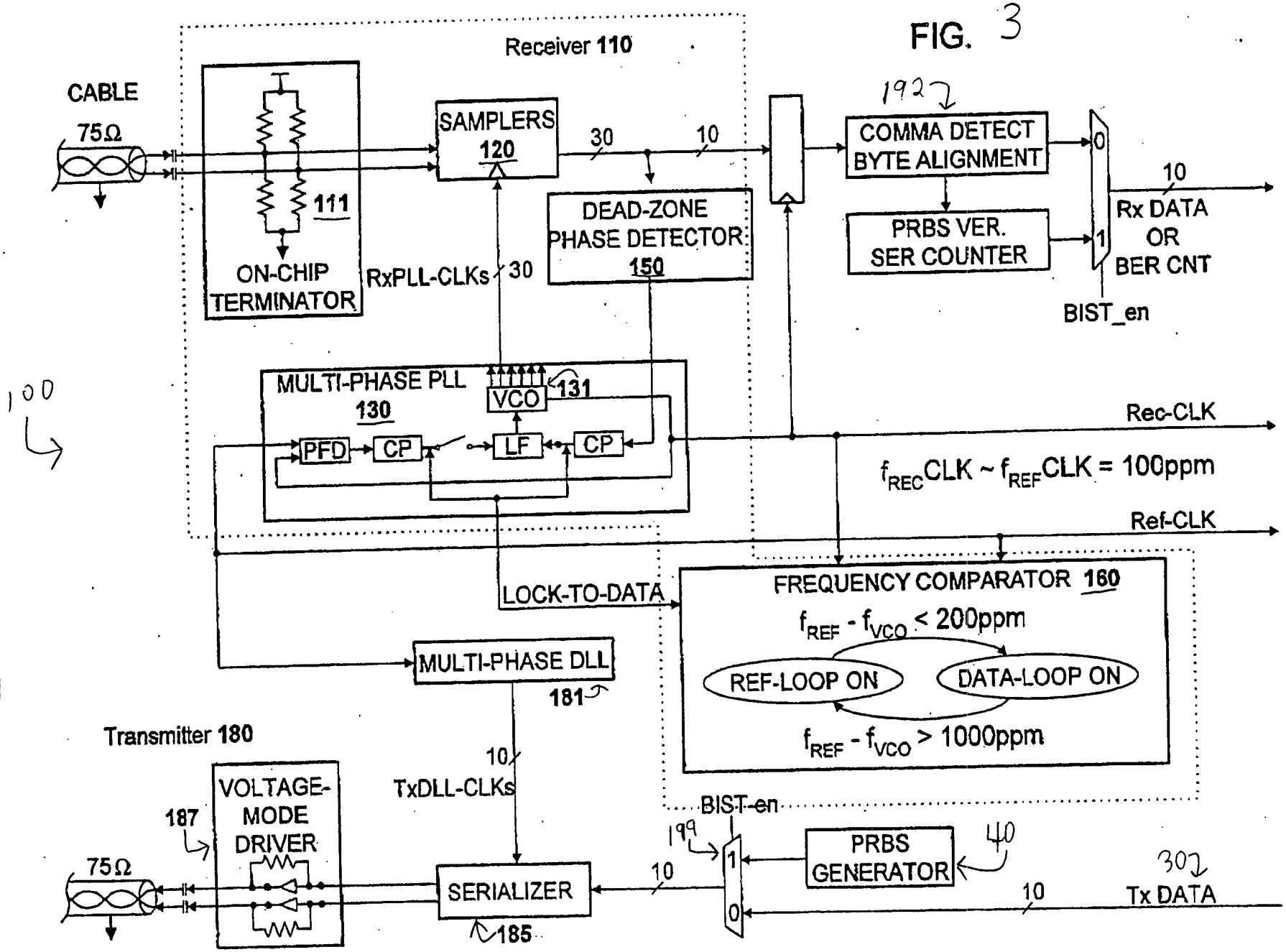


FIGURE 2

Title: "Tracked 3X Oversampling Receiver"
Inventors: MOON et al.
Attorney Docket No.: 59472-8086.US03
Express Mail Label: EV 099149570 US [Sheet 2 of 11]

Title: "Tracked 3X Oversampling Receiver"
Inventors: MOON et al.
Attorney Docket No.: 59472-8086.US03
Express Mail Label: EV 099149570 US [Sheet 3 of 11]

FIG. 3



Title: "Tracked 3X Oversampling Receiver"
Inventors: MOON et al.
Attorney Docket No.: 59472-8086.US03
Express Mail Label: EV 099149570 US [Sheet 4 of 11]

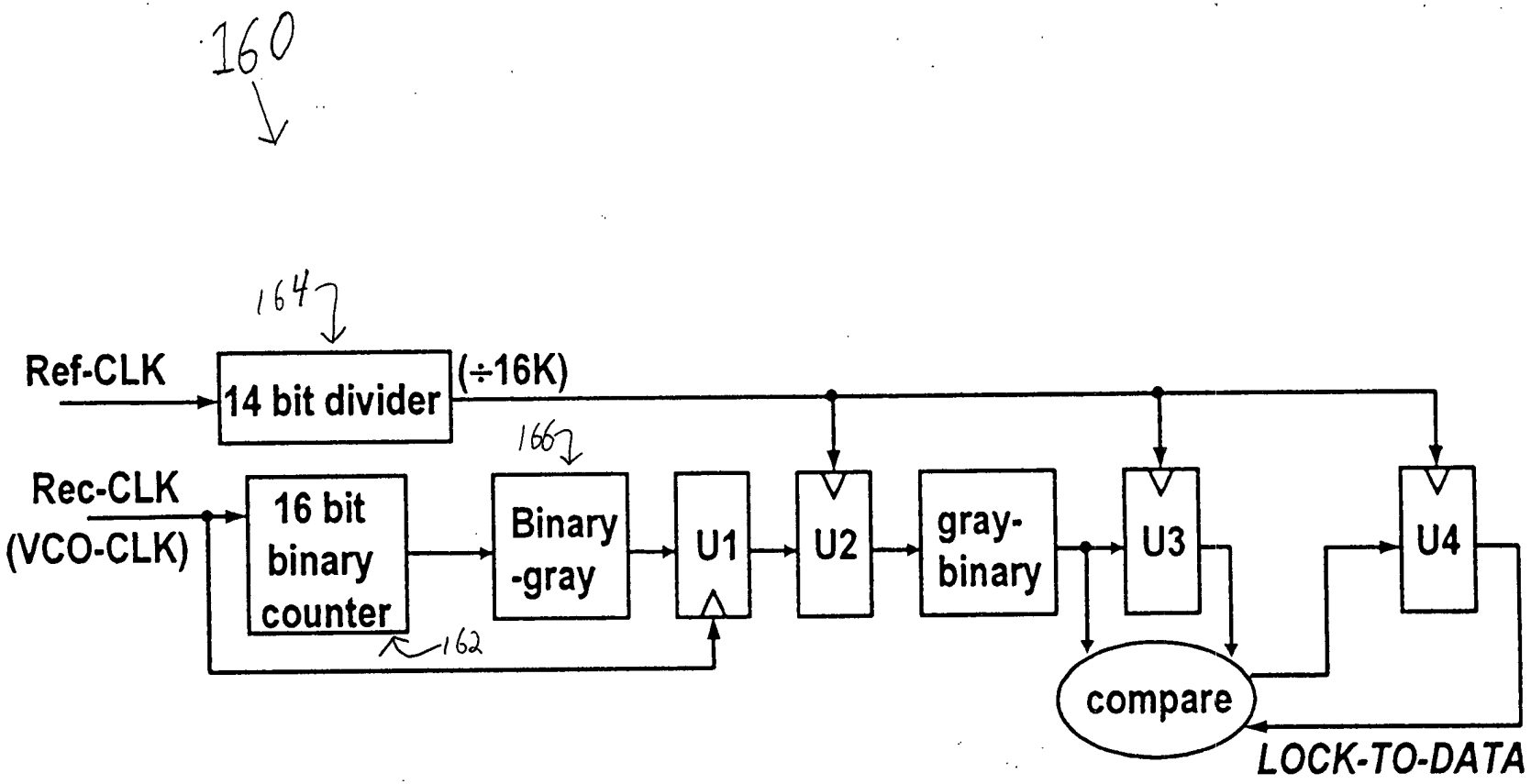
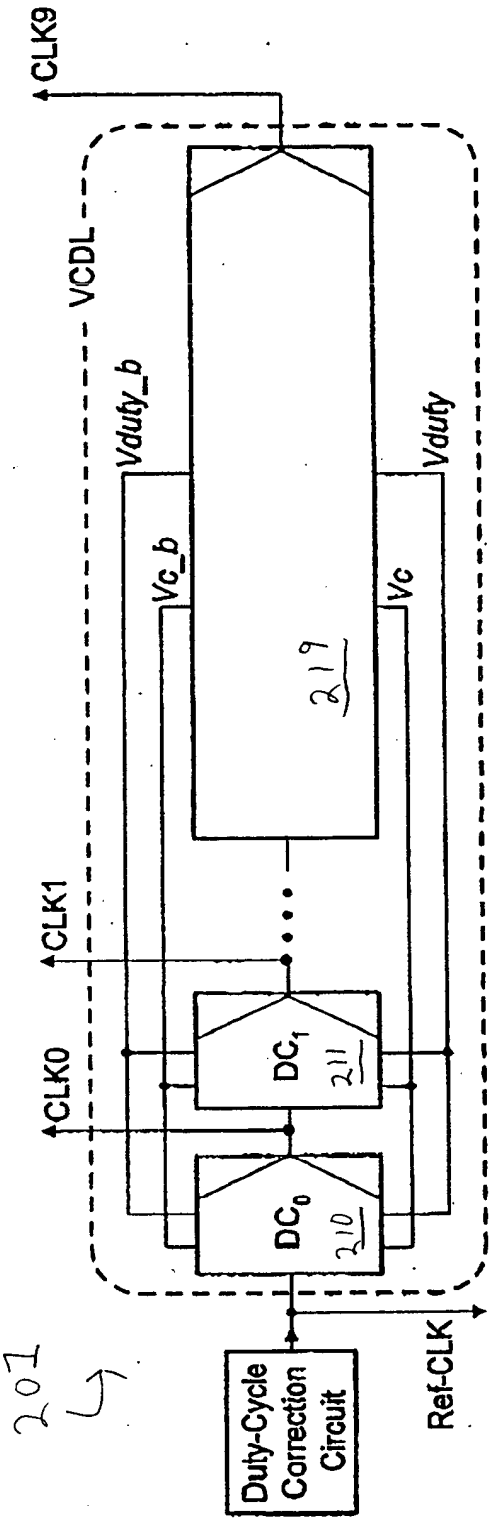


FIGURE 4

FIG. 5A


CLK9
VCDL
Vduty_b
Vc_b
Vc
Vduty
219
CLK1
CLK0
DC1
211
DC0
210
Duty-Cycle Correction Circuit
Ref-CLK
207

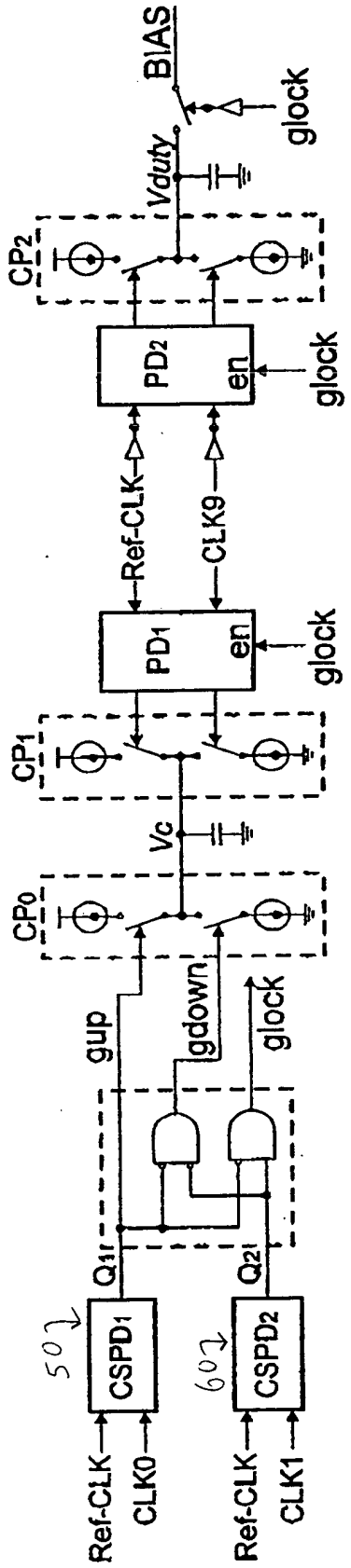
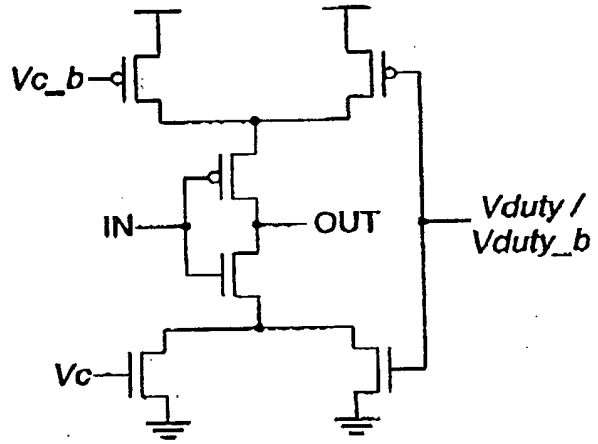

BIAS
glock
Vduty
CP2
PD2
en
glock
Ref-CLK
CLK9
PD1
en
glock
CP1
Vc
CP0
gup
gdown
glock
Q1
Q2
CSPD1
CSPD2
502
602
Ref-CLK
CLK0
Ref-CLK
CLK1

FIG. 5B



210

FIG. 5C

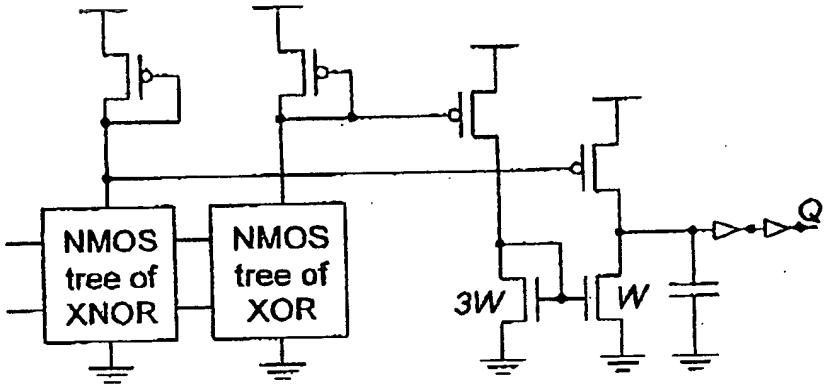


FIG. 5D

50

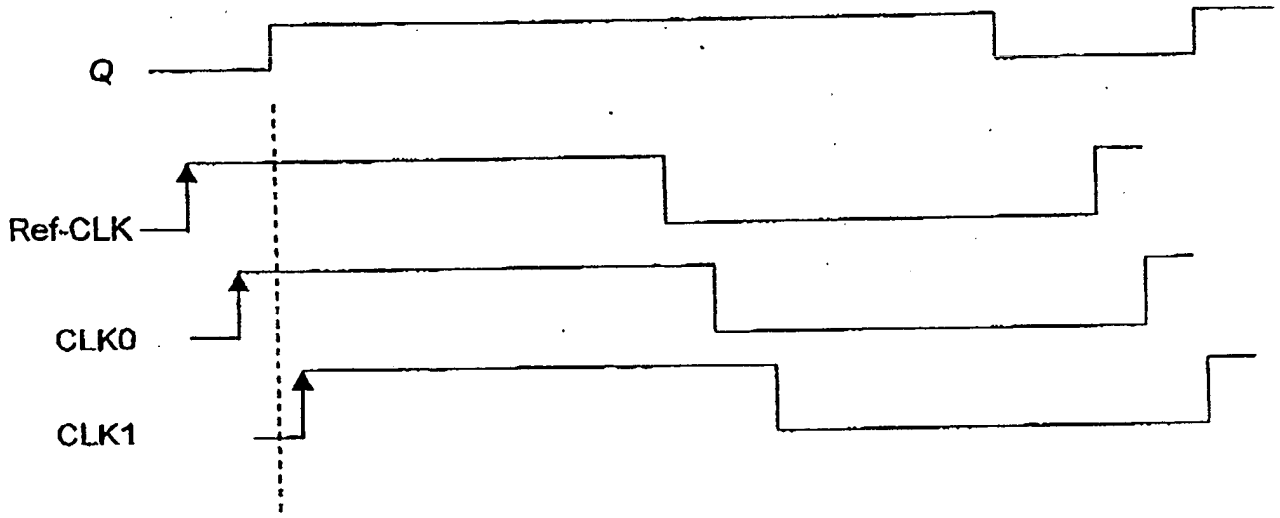
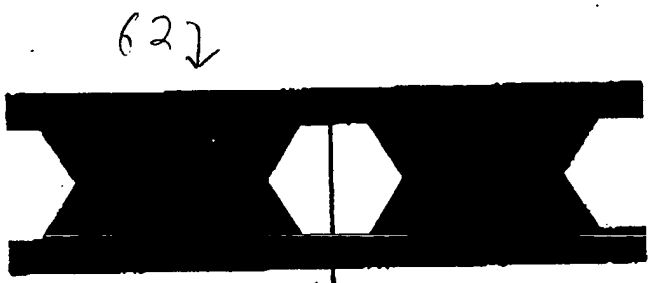
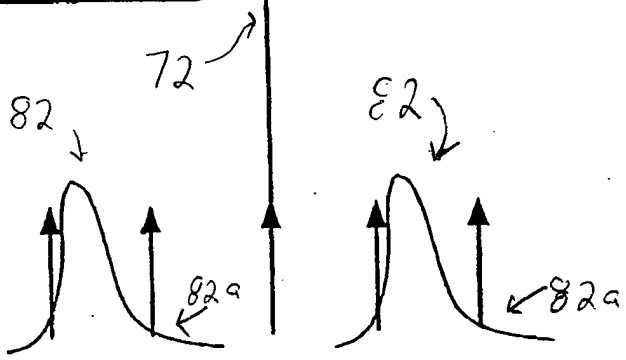


FIG. 5E

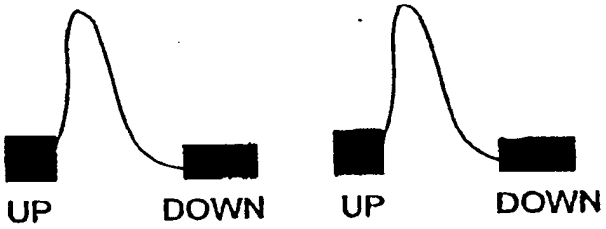
Eye diagram
FIG. 6A



Jitter histogram &
Sampling clocks
FIG. 6B



# of UP pulses &
# of DOWN pulses
FIG. 6C



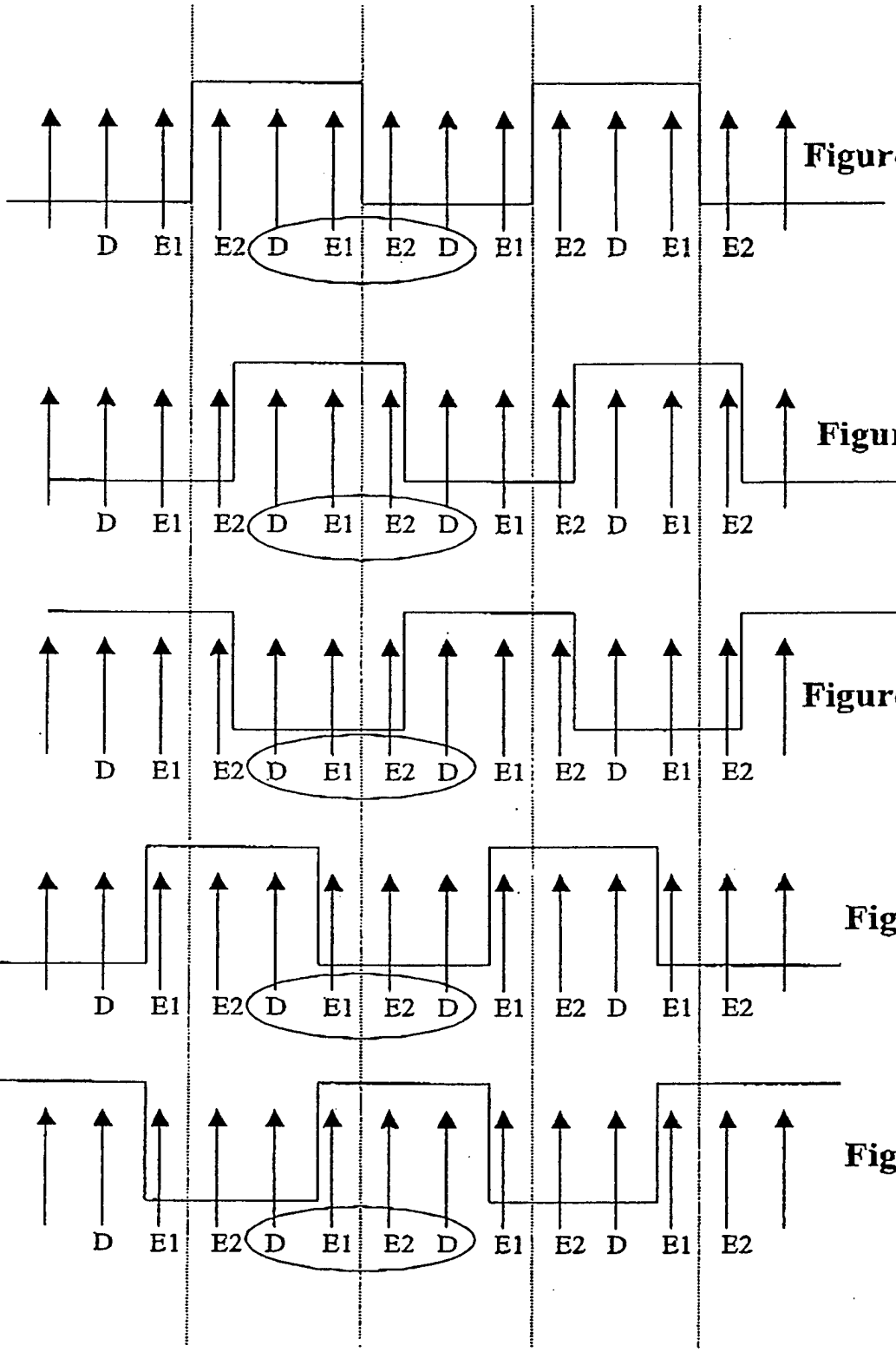


Figure 7A

Figure 7B

Figure 7C

Figure 7D

Figure 7E

Title: "Tracked 3X Oversampling Receiver"
Inventors: MOON et al.
Attorney Docket No.: 59472-8086.US03
Express Mail Label: EV 099149570 US [Sheet 11 of 11]

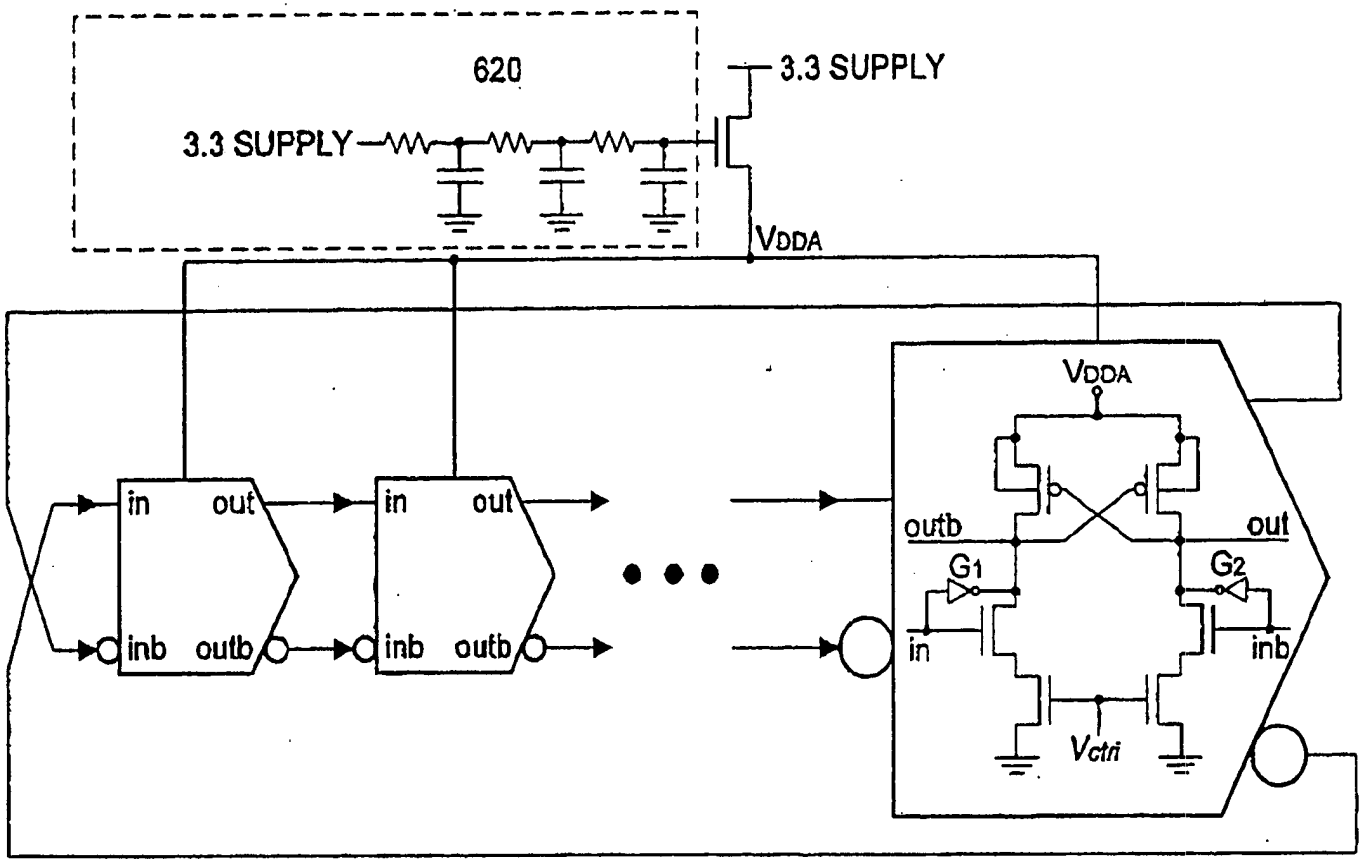


FIG. 8